

Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)	ATTY. DKT. NO. 5181-99001	SERIAL NO. 10/821,393 GROUP: 2186
	APPLICANT: Landin, et al.	
	FILING DATE: April 9, 2003	

U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
SE	A1	6,209,064	3/27/01	Weber			
SE	A2	6,088,768	7/11/00	Baldus, et al			
SE	A3	5,802,582	9/1/98	Ekanadham, et al			
SE	A4	5,761,721	6/2/98	Baldus, et al			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

SE	A5	"Specifying and Verifying a Broadcast and a Multicast Snooping Cache Coherence Protocol", Sorin, et al, <u>IEEE Transactions on Parallel and Distributed Systems</u> , Vol. 13, No. 6, June 2002, http://www.cs.wisc.edu/multifacet/papers/tpds02_lamport.pdf
SE	A6	"Multicast Snooping: A New Coherence Method Using a Multicast Address Network", Bilir, et al, <u>The 26th International Symposium on Computer Architecture</u> , IEEE, Atlanta, GA, May 2-4, 1999, http://csdl.computer.org/comp/proceedings/isca/1999/0170/00/01700294abs.htm
SE	A7	"Architecture and Design of AlphaServer GS320", Ghárachorloo, et al, <u>ACM Sigplan Notices</u> , Volume 35, Issue 11, November 2000, http://portal.acm.org/citation.cfm?id=356991&dl=ACM&coll=portal
SE	A8	"View Caching: Efficient Software Shared Memory for Dynamic Computations", Karamcheti, et al, <u>11th International Parallel Processing Symposium</u> , Geneva, Switzerland, April 1-5, 1997, http://ipdps.eece.unm.edu/1997/s13/318.pdf
SE	A9	"Cache-Coherent Distributed Shared Memory: Perspectives on Its Development and Future Challenges", Hennessy, et al, <u>Proceedings of the IEEE</u> , Vol. 87, Issue 3, March 1999, ISSN 0018-9219, http://cva.stanford.edu/cs99s/papers/hennessy-cc.pdf
SE	A10	"Survey on Cache Coherence in Shared & Distributed Memory Multiprocessors", Garg, et al, Online, http://www.cse.psu.edu/~cg530/proj03/cache_coherence.pdf
SE	A11	"A Survey of Cache Coherence Mechanisms in Shared Memory Multiprocessors", Lawrence, Department of Computer Science, University of Manitoba, Manitoba, Canada, May 14, 1998, http://www.cs.uiowa.edu/~rlawrenc/research/Papers/cc.pdf
SE	A12	"Bandwidth Adaptive Snooping", Martin, et al. <u>8th Annual International Symposium on High-Performance Computer Architecture (HPCA-8)</u> , Cambridge, MA, February 2-6, 2002.
SE	A13	"Timestamp Snooping: An Approach for Extending SMPs", Martin, et al., <u>9th International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS-IX)</u> , Cambridge, MA, November 13-15, 2000.

EXAMINER: /Shawn Eland/

DATE CONSIDERED:

01/09/2007

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.